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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/808,790	03/15/2001	Krishna Vepa	20468-000110	5617
20350	7590	05/07/2003		
TOWNSEND AND TOWNSEND AND CREW, LLP TWO EMBARCADERO CENTER EIGHTH FLOOR SAN FRANCISCO, CA 94111-3834			EXAMINER	
			VINH, LAN	
		ART UNIT	PAPER NUMBER	
		1765		

DATE MAILED: 05/07/2003

Please find below and/or attached an Office communication concerning this application or proceeding.

<b>Office Action Summary</b>	Application No.	Applicant(s)
	09/808,790	VEPA ET AL.
Examiner	Art Unit	
Lan Vinh	1765	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

#### Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

#### Status

- 1) Responsive to communication(s) filed on 15 March 2001.
- 2a) This action is FINAL.      2b) This action is non-final.
- 3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

#### Disposition of Claims

- 4) Claim(s) 1-25 is/are pending in the application.
- 4a) Of the above claim(s) 19-25 is/are withdrawn from consideration.
- 5) Claim(s) 1-17 is/are allowed.
- 6) Claim(s) 18 is/are rejected.
- 7) Claim(s) \_\_\_\_\_ is/are objected to.
- 8) Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

#### Application Papers

- 9) The specification is objected to by the Examiner.
- 10) The drawing(s) filed on \_\_\_\_\_ is/are: a) accepted or b) objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- 11) The proposed drawing correction filed on \_\_\_\_\_ is: a) approved b) disapproved by the Examiner.  
If approved, corrected drawings are required in reply to this Office action.
- 12) The oath or declaration is objected to by the Examiner.

#### Priority under 35 U.S.C. §§ 119 and 120

- 13) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) All b) Some \* c) None of:  
 1. Certified copies of the priority documents have been received.  
 2. Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.  
 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- \* See the attached detailed Office action for a list of the certified copies not received.
- 14) Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).  
a) The translation of the foreign language provisional application has been received.
- 15) Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.

#### Attachment(s)

- 1) Notice of References Cited (PTO-892)      4) Interview Summary (PTO-413) Paper No(s). \_\_\_\_\_
- 2) Notice of Draftsperson's Patent Drawing Review (PTO-948)      5) Notice of Informal Patent Application (PTO-152)
- 3) Information Disclosure Statement(s) (PTO-1449) Paper No(s) 5.      6) Other: \_\_\_\_\_

## DETAILED ACTION

### ***Information Disclosure Statement***

1. The Information Disclosure Statement (IDS) filed on 4/9/2003 have been considered. The PTO/SB/08B forms are enclosed in this office action.

### ***Specification***

2. The applicants are requested to provide the US Patent Application Serial number listed on page 12 of the specification.

### ***Election/Restrictions***

3. Applicant's election without traverse of Group I, claims 1-18 in Paper No. 4 is acknowledged.

### ***Claim Rejections - 35 USC § 102***

4. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

5. Claim 18 is rejected under 35 U.S.C. 102(e) as being anticipated by Vandamme et al (US 6,114,245)

Vandamme discloses a method for processing semiconductor wafer comprises the steps of:

providing a wafer has a peripheral edge and opposite front and back surface (col 3, lines 21-23), which reads on providing a wafer having first and second surface and a peripheral edge

then grinding the front/first surface and back/second wafer surface (col 3, lines 34-36)

profiling the edge peripheral of the wafer (col 3, lines 31-32)

polishing the edge/peripheral of the wafer (col 6, lines 4-5)

polishing both sides surfaces of the wafer (col 6, lines 4-6), which reads on polishing the first and second wafer surfaces.

### ***Allowable Subject Matter***

6. Claims 1-17 are allowed.

The following is an examiner's statement of reasons for allowance:

Regarding claim 1, no prior art of record discloses processing the wafer through a second module, said second module comprising apparatus for performing a double side polish (DSP) process, a clean process and a metrology process and processing the wafer through the DSP process, the clean process and the metrology. The closest prior art of Bowman et al (6,309,279) discloses processing the wafer through a first module that has no metrology apparatus and processing the wafer through a second module, said second module comprising a number of varied operation complementing the

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polishing operation such as rinsing, drying, scrubbing, etching and metrology equipment

without suggesting the use of a double side polish (DSP) process in the second module.

Vasat et al (US 6,376,395) discloses processing the wafer through a DSP process, a grinding process, a cleaning process without using a metrology process.

### ***Conclusion***

7. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Lan Vinh whose telephone number is 703 305-6302.

The examiner can normally be reached on M-F 8:30-5:30 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Benjamin Utech can be reached on 703 308-3836. The fax phone numbers for the organization where this application or proceeding is assigned are 703 872-9310 for regular communications and 703 872-9311 for After Final communications.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is 703 308-0661.



LV

May 5, 2003